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- \* Bundled with an optional Intel supplement
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Designed as an introductory text for the students of computer science, computer applications, electronics engineering and information technology for their first course on the organization and architecture of computers, this accessible, student friendly text gives a clear and in-depth analysis of the basic principles underlying the subject. This self-contained text devotes one full chapter to the basics of digital logic. While the initial chapters describe in detail about computer organization, including CPU design, ALU design, memory design and I/O organization, the text also deals with Assembly Language Programming for Pentium using NASM assembler. What distinguishes the text is the special attention it pays to Cache and Virtual Memory organization, as well as to RISC architecture and the intricacies of pipelining. All these discussions are climaxed by an illuminating discussion on parallel computers which shows how processors are interconnected to create a variety of parallel computers. **KEY FEATURES** ? Self-contained presentation starting with data representation and ending with advanced parallel computer architecture. ? Systematic and logical organization of topics. ? Large number of worked-out examples and exercises. ? Contains basics of assembly language programming. ? Each chapter has learning objectives and a detailed summary to help students to quickly revise the material.

**COMPUTER ORGANIZATION AND ARCHITECTURE: THEMES AND VARIATIONS** stresses the structure of the complete system (CPU, memory, buses and peripherals) and reinforces that core content with an emphasis on divergent examples. This approach to computer architecture is an effective arrangement that provides sufficient detail at the logic and organizational levels appropriate for EE/ECE departments as well as for Computer Science readers. The text goes well beyond the minimal curriculum coverage and introduces topics that are important to anyone involved with computer architecture in a

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way that is both thought provoking and interesting to all. Important Notice: Media content referenced within the product description or the product text may not be available in the ebook version.

With the introduction of the 4004 microprocessor by Intel in 1971, a new era of computing power began, which flourished with devices like the 8085 and 8086. PCs became available in the market, their processing power enhanced every time a new processor was available to system designers. The reason behind the introduction of computers from the IBM PC, PC/XT, PC/AT to the latest laptops and think-pads may be attributed to the introduction of processors like the 8088, 80286, 80386, Pentium and Core2Duo. Computer Organization and Architecture: From 8085 to Core2Duo & Beyond (For JNTU) deals with external and internal features of these computers, taking into account the control unit (CU), processor details and their instruction sets, memory organization, external interfacing bus with standard input/output devices like the optical mouse or TFT screen, pipelining and parallel processing. Both modern as well as classical concepts are discussed with adequate weightage, and compared, as and when necessary.

MCQs (Multiple Choice Questions) in COMPUTER ORGANIZATION is a comprehensive questions answers quiz book for undergraduate students. This quiz book comprises question on COMPUTER ORGANIZATION practice questions, COMPUTER ORGANIZATION test questions, fundamentals of COMPUTER ORGANIZATION practice questions, COMPUTER ORGANIZATION questions for competitive examinations and practice questions for COMPUTER ORGANIZATION certification. In addition, the book consists of Sufficient number of COMPUTER ORGANIZATION MCQ (multiple choice questions) to understand the concepts better. This book is essential for students preparing for

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Digital Design and Computer Architecture: ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of this book, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing an ARM processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs)—SystemVerilog and VHDL—which illustrate and compare the ways each can be used

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in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises.

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