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This book describes the recent innovation of deep in-memory architectures for realizing AI systems that operate at the edge of energy-latency-accuracy

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billions of
sub-100-nanometer
transistors. By 2020,
however, we will see systems-
on-chips with trillions of
10-nanometer transistors.
But this will be the end of
the miniaturization, because

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yet smaller transistors,
containing just a few
control atoms, are subject
to statistical fluctuations
and thus no longer useful.
We also need to worry about
a potential energy crisis,
because in less than five

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property; contracts; torts;
and crime and punishment.
Added examples and cases
help to clarify economic
applications further.

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This book is a collection of selected peer-reviewed papers presented at the International Conference on Signal Processing and Communication (ICSC 2018). It covers current research

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and developments in the fields of communications, signal processing, VLSI circuits and systems, and embedded systems. The book offers in-depth discussions and analyses of latest problems across different

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sub-fields of signal processing and communications. The contents of this book will prove to be useful for students, researchers, and professionals working in electronics and electrical

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engineering, as well as
other allied fields.

This book includes a
selection of papers from the
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Conference on Software
Process Improvement

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(CIMPS'17), presenting trends and applications in software engineering. Held from 18th to 20th October 2017 in Zacatecas, Mexico, the conference provided a global forum for researchers and practitioners to present

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and discuss the latest innovations, trends, results, experiences and concerns in various areas of software engineering, including but not limited to software processes, security in information and

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communication technology,
and big data. The main
topics covered are
organizational models,
standards and methodologies,
software process
improvement, knowledge
management, software

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systems, applications and tools, information and communication technologies and processes in non-software domains (mining, automotive, aerospace, business, health care, manufacturing, etc.) with a

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demonstrated relationship to software engineering challenges.

"In the last few years, power dissipation has become an important design constraint, on par with

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reduce both dynamic power and static power dissipation in processors and memory hierarchies. A significant number of techniques have been proposed for a wide range of situations and this book synthesizes those

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techniques by focusing on
their common
characteristics." - -BOOK
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Covering both the classical
and emerging nanoelectronic
technologies being used in

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mixed-signal design, this book addresses digital, analog, and memory components. Winner of the Association of American Publishers' 2016 PROSE Award in the Textbook/Physical Sciences & Mathematics

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the director of the NanoSystem Design Laboratory at the University of North Texas, this comprehensive guide provides a large-scale picture of the design and manufacturing aspects of nanoelectronic-based

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design are discussed. Top-down analysis of all stages--from design to manufacturing Coverage of current and developing nanoelectronic technologies--not just nano-CMOS Describes the basics of

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nanoelectronic technology
and the structure of popular
electronic systems Reveals
the techniques required for
design excellence and
manufacturability

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best selected research papers presented at 6th International Conference on Innovations in Electronics and Communication Engineering at Guru Nanak Institutions Hyderabad, India. The book presents

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works from researchers,
technocrats and experts
about latest technologies in
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various streams of
communication engineering
like signal processing, VLSI

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design, embedded systems,
wireless communications, and
electronics and
communications in general.
The authors have discussed
the latest cutting edge
technology and the volume
will serve as a reference

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for young researchers.**

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